

PTC thermistors for overcurrent protection

SMDs, EIA sizes 0603 and 1210, 24 V, 42 V, 63 V and 230 V $\,$

Series/Type:

Date: January 2009

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SMD

Applications

- Overcurrent protection
- Short circuit protection

Features

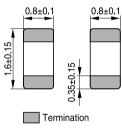
- Thermistor chip with lead-free tinned terminations
- Small size
- Short response times
- Suitable for reflow soldering only
- Suitable for automatic placement
- RoHS-compatible

Delivery mode

 Blister tape (case size 1210) or cardboard tape (case size 0603), 180-mm reel with 8-mm tape, taping to IEC 60286-3

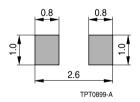
Dimensional drawings in mm

EIA case size 0603

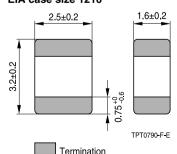


TPT0698-5-E

Solder pad

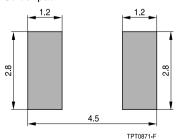


EIA case size 1210



Recommended maximum dimensions (mm)

Solder pad





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General technical data

Switching cycles		N	100	
Tolerance of R _R	(except A907)	ΔR_R	±25	%
Tolerance of R _R	(for A907)	ΔR_R	±35	%
Operating temperature range	(V = 0)	T _{op}	-40/+125	°C
Operating temperature range	$(V = V_{max}, except A907)$	T _{op}	-20/+85	°C
Operating temperature range	$(V = V_{max}, for A907)$	T _{op}	-40/+85	°C

Electrical specifications and ordering codes

	1 1)	1.4\	Ι,	Б			
Type	I _R 1)	I _S 1)	I _{Smax}	R_R	R _{min}	EIA	Ordering code
			$(V = V_{max})$			case	
	mA	mA	Α	Ω	Ω	size	
$V_{\text{max}} = 30 \text{ V DC or V AC}, V_{\text{R}} = 24 \text{ V DC or V AC}$							
A606	90	180	0.5	27	17	1210	B59606A0110A062
A607	70	130	0.4	55	30	1210	B59607A0120A062
$V_{\text{max}} = 60 \text{ V DC or V AC}, V_{\text{R}} = 42 \text{ V DC or V AC}$							
A622	22	47	0.22	220	150	0603	B59622A0090A062
$V_{\text{max}} = 80 \text{ V DC or V AC}, V_{\text{R}} = 63 \text{ V DC or V AC}$							
A623	15	33	0.15	470	300	0603	B59623A0090A062
A707	50	90	0.3	125	75	1210	B59707A0120A062
$V_{\text{max}} = 265 \text{ V DC or V AC}, V_{\text{R}} = 230 \text{ V DC or V AC}$							
A807	15	40	0.2	400	200	1210	B59807A0090A062
$V_{\text{max}} = 400 \text{ V DC or V AC}, V_{\text{R}} = 230 \text{ V DC or V AC}$							
A907	12	22	0.15	1500	640	1210	B59907A0120A062

¹⁾ Measured on component soldered to standardized PCB



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Reliability data

Test	Standard	Test conditions	$ \Delta R_{25}/R_{25} $
Electrical endurance,	IEC 60738-1	Room temperature, I _{Smax} ; V _{max}	< 25%
cycling		Number of cycles: 100	
Electrical endurance,	IEC 60738-1	Storage at V _{max} /T _{op,max} (V _{max})	< 25%
constant		Test duration: 1000 h	
Damp heat	IEC 60738-1	Temperature of air: 40 °C	< 10%
		Relative humidity of air: 93%	
		Duration: 56 days	
		Test according to IEC 60068-2-78	
Rapid change	IEC 60738-1	$T_1 = T_{op,min} (0 \text{ V}), T_2 = T_{op,max} (0 \text{ V})$	< 10%
of temperature		Number of cycles: 5	
		Test duration: 30 min	
		Test according to IEC 60068-2-14, Test Na	
Vibration	IEC 60738-1	Frequency range: 10 to 55 Hz	< 5%
		Displacement amplitude: 0.75 mm	
		Test duration: 3 × 2 h	
1		Test according to IEC 60068-2-6, Test Fc	
Shock	IEC 60738-1	Acceleration: 390 m/s ²	< 5%
		Pulse duration: 6 ms; 6 × 4000 pulses	
Climatic sequence	IEC 60738-1	Dry heat: $T = T_{op,max}(0 \text{ V})$	< 10%
		Test duration: 16 h	
		Damp heat first cycle	
		Cold: $T = T_{op,min}(0 \text{ V})$	
		Test duration: 2 h	
		Damp heat 5 cycles	
		Tests performed according to	
		IEC 60068-2-30	
Bending test	EN 130000/4.35	Components reflow-soldered to test board	< 10%
		Maximum bending: 2 mm	
Adhesive strength on		A shear force of 5 N is applied	No visible
PCB		perpendicular to the longitudinal axis of the	damage
		component which is soldered on PCB.	



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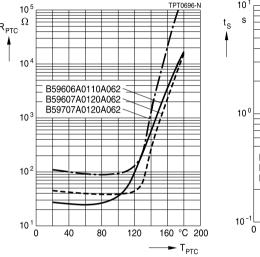
SMD

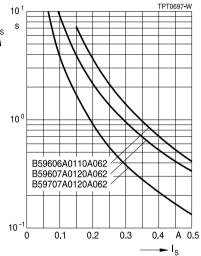
Characteristics (typical) for A606, A607 and A707

PTC resistance R_{PTC} versus PTC temperature T_{PTC}

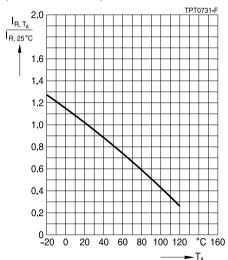
(measured at low signal voltage) TPT0696-N 10⁵ $\mathsf{R}_{\mathsf{PTC}}^{\phantom{\mathsf{PTC}}\Omega}$

Switching time ts versus switching current Is (measured at 25 °C in still air)





Rated current I_R versus ambient temperature T_A (measured in still air)



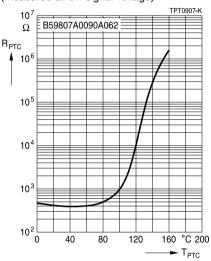


SMDs, EIA sizes 0603 and 1210, 24 V, 42 V, 63 V and 230 V

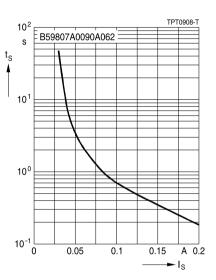
SMD

Characteristics (typical) for A807

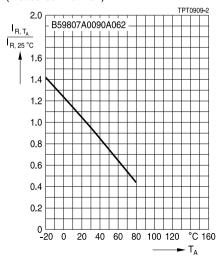
PTC resistance R_{PTC} versus PTC temperature T_{PTC} (measured at low signal voltage)



Switching time t_{S} versus switching current I_{S} (measured at 25 °C in still air)



Rated current I_R versus ambient temperature T_A (measured in still air)



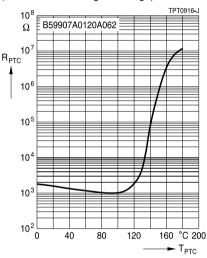


SMDs, EIA sizes 0603 and 1210, 24 V, 42 V, 63 V and 230 V

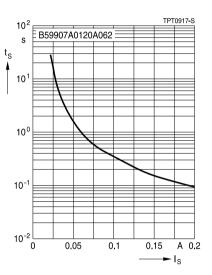
SMD

Characteristics (typical) for A907

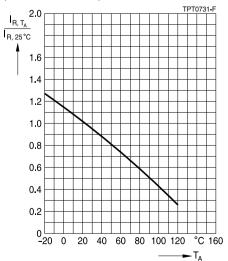
PTC resistance R_{PTC} versus PTC temperature T_{PTC} (measured at low signal voltage)



Switching time t_S versus switching current I_S (measured at 25 °C in still air)



Rated current I_R versus ambient temperature T_A (measured in still air)





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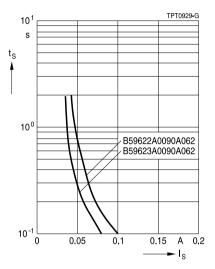
Characteristics (typical) for A622 and A623

PTC resistance R_{PTC} versus PTC temperature T_{PTC} (measured at low signal voltage)

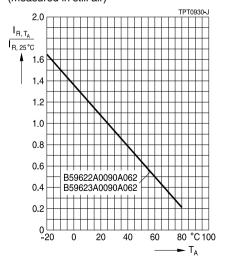
R_{PTC}
10⁴
B59622A0090A062
B59623A0090A062
10³
10²
10¹
-40 -20 0 20 40 60 80 100 120 °C 160

— Τ_{PTC}

Switching time t_S versus switching current I_S (measured at 25 °C in still air)



Rated current I_R versus ambient temperature T_A (measured in still air)





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Cautions and warnings

General

- EPCOS thermistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- Ensure suitability of thermistor through reliability testing during the design-in phase. The thermistors should be evaluated taking into consideration worst-case conditions.

Storage

- Store thermistors only in original packaging. Do not open the package before storage.
- Storage conditions in original packaging: storage temperature −25 °C ... +45 °C, relative humidity ≤75% annual mean, maximum 95%, dew precipitation is inadmissible.
- Avoid contamination of thermistors surface during storage, handling and processing.
- Avoid storage of thermistor in harmful environment with effect on function on long-term operation (examples given under operation precautions).
- Use thermistor within 6 months after delivery.

Handling

- PTCs must not be dropped. Chip-offs must not be caused during handling of PTCs.
- Components must not be touched with bare hands. Gloves are recommended.
- Avoid contamination of thermistor surface during handling.

Soldering (where applicable)

- Use rosin-type flux or non-activated flux.
- Insufficient preheating may cause ceramic cracks.
- Rapid cooling by dipping in solvent is not recommended.
- Complete removal of flux is recommended.
- Standard PTC heaters are not suitable for soldering.



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Mounting

- Electrode must not be scratched before/during/after the mounting process.
- Contacts and housing used for assembly with thermistor have to be clean before mounting. Especially grease or oil must be removed.
- When PTC thermistors are encapsulated with sealing material, the precautions given in chapter "Mounting instructions", "Sealing and potting" must be observed.
- When the thermistor is mounted, there must not be any foreign body between the electrode of the thermistor and the clamping contact.
- The minimum force of the clamping contacts pressing against the PTC must be 10 N.
- During operation, the thermistor's surface temperature can be very high. Ensure that adjacent components are placed at a sufficient distance from the thermistor to allow for proper cooling at the thermistors.
- Ensure that adjacent materials are designed for operation at temperatures comparable to the surface temperature of thermistor. Be sure that surrounding parts and materials can withstand this temperature.
- Avoid contamination of thermistor surface during processing.

Operation

- Use thermistors only within the specified temperature operating range.
- Use thermistors only within the specified voltage and current ranges.
- Environmental conditions must not harm the thermistors. Use thermistors only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions. Contact with any liquids and solvents should be prevented.
- Be sure to provide an appropriate fail-safe function to prevent secondary product damage caused by abnormal function (e.g. use VDR for limitation of overvoltage condition).



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Symbols and terms

A Area

 $\begin{array}{ll} C_{th} & & \text{Heat capacity} \\ f & & \text{Frequency} \\ I & & \text{Current} \end{array}$

 I_{max}
 Maximum current

 I_R
 Rated current

 I_{PTC}
 PTC current

 I.
 Residual currrent

 $\begin{array}{ll} I_{\text{r,oil}} & \text{Residual currrent in oil (for level sensors)} \\ I_{\text{r,air}} & \text{Residual currrent in air (for level sensors)} \\ I_{\text{BMS}} & \text{Root-mean-square value of current} \end{array}$

Is Switching current

I_{Smax} Maximum switching current LCT Lower category temperature

N Number (integer)

N_c Operating cycles at V_{max}, charging of capacitor

N_f Switching cycles at V_{max}, failure mode

P Power

P₂₅ Maximum power at 25 °C

 P_{el} Electrical power Pdies Dissipation power R_{min} Minimum resistance R_{R} Rated resistance ΔR_{R} Tolerance of R_R Parallel resistance R_P R_{PTC} PTC resistance R_{ref} Reference resistance Series resistance R_s

Resistance matching per reel/ packing unit at 25 °C

 ΔR_{25} Tolerance of R_{25} T Temperature

t Time

 R_{25}

T_A Ambient temperaturet_a Thermal threshold time

T_C Ferroelectric Curie temperature

Resistance at 25 °C



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t_E Settling time (for level sensors)

 $\begin{array}{lll} T_{\text{R}} & & \text{Rated temperature} \\ T_{\text{sense}} & & \text{Sensing temperature} \\ T_{\text{op}} & & \text{Operating temperature} \\ T_{\text{PTC}} & & \text{PTC temperature} \end{array}$

t_R Response time

 T_{ref} Reference temperature

T_{Rmin} Temperature at minimum resistance

t_s Switching time

T_{surf} Surface temperature

UCT Upper category temperature

V or V_{el} Voltage (with subscript only for distinction from volume)

V_{RMS} Root-mean-square value of voltage

 $\begin{array}{lll} V_{\text{BD}} & & \text{Breakdown voltage} \\ V_{\text{ins}} & & \text{Insulation test voltage} \\ V_{\text{link,max}} & & \text{Maximum link voltage} \\ V_{\text{max}} & & \text{Maximum operating voltage} \end{array}$

V_{max dvn} Maximum dynamic (short-time) operating voltage

V_{meas} Measuring voltage

V_{meas,max} Maximum measuring voltage

V_B Rated voltage

V_{PTC} Voltage drop across a PTC thermistor

 $\begin{array}{lll} \alpha & & \text{Temperature coefficient} \\ \Delta & & \text{Tolerance, change} \\ \delta_{\text{th}} & & \text{Dissipation factor} \end{array}$

 τ_{th} Thermal cooling time constant

λ Failure rate

e Lead spacing (in mm)

Abbreviations / Notes

SMD Surface-mount devices

* To be replaced by a number in ordering codes, type designations etc.

+ To be replaced by a letter

All dimensions are given in mm.

The commas used in numerical values denote decimal points.



Important notes

The following applies to all products named in this publication:

- 1. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule, EPCOS is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an EPCOS product with the properties described in the product specification is suitable for use in a particular customer application.
- 2. We also point out that in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or lifesaving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
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